

FIG. 1

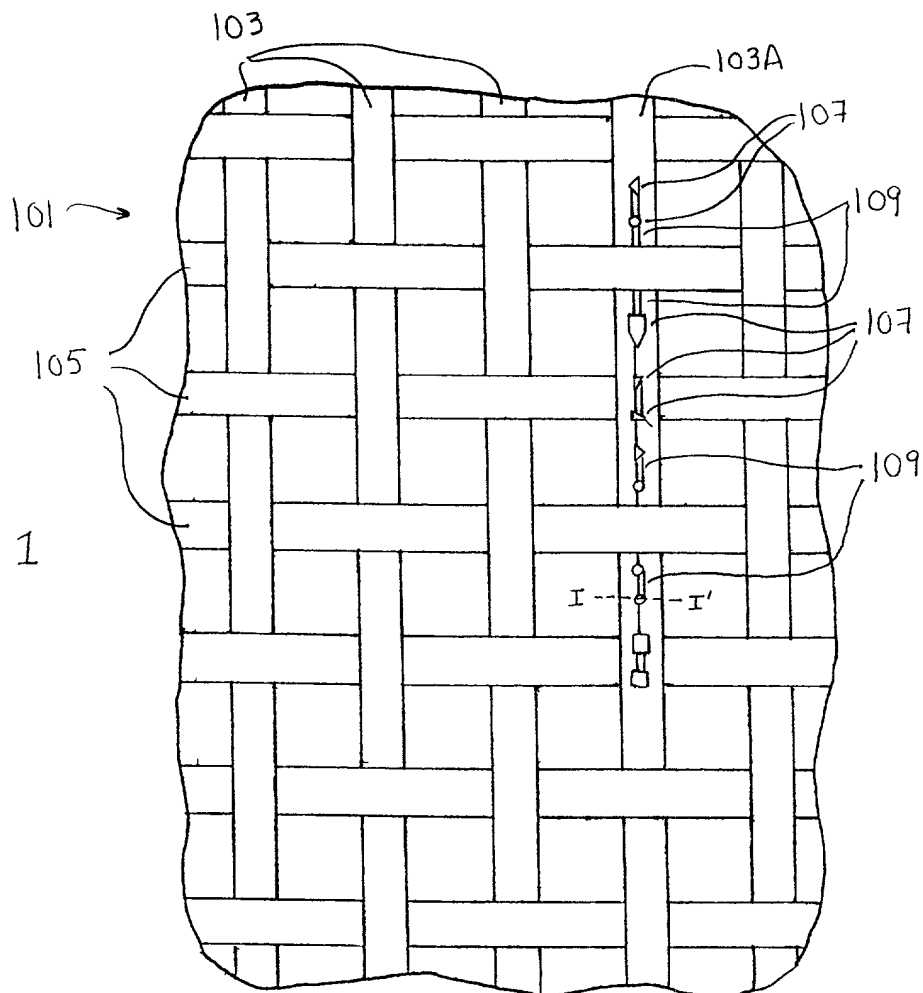
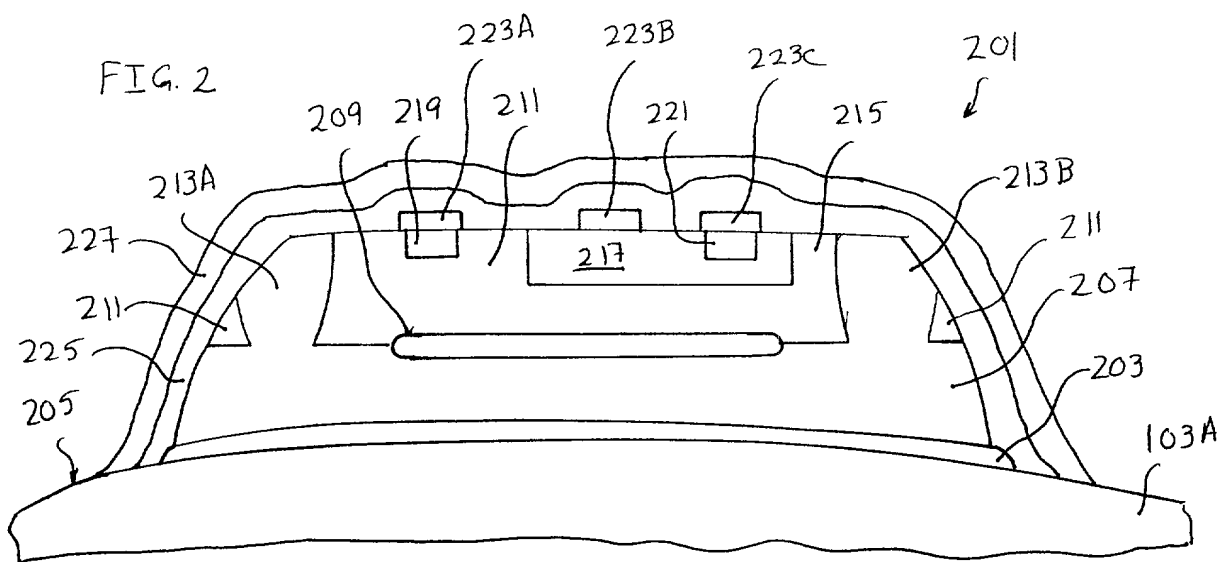


FIG. 2



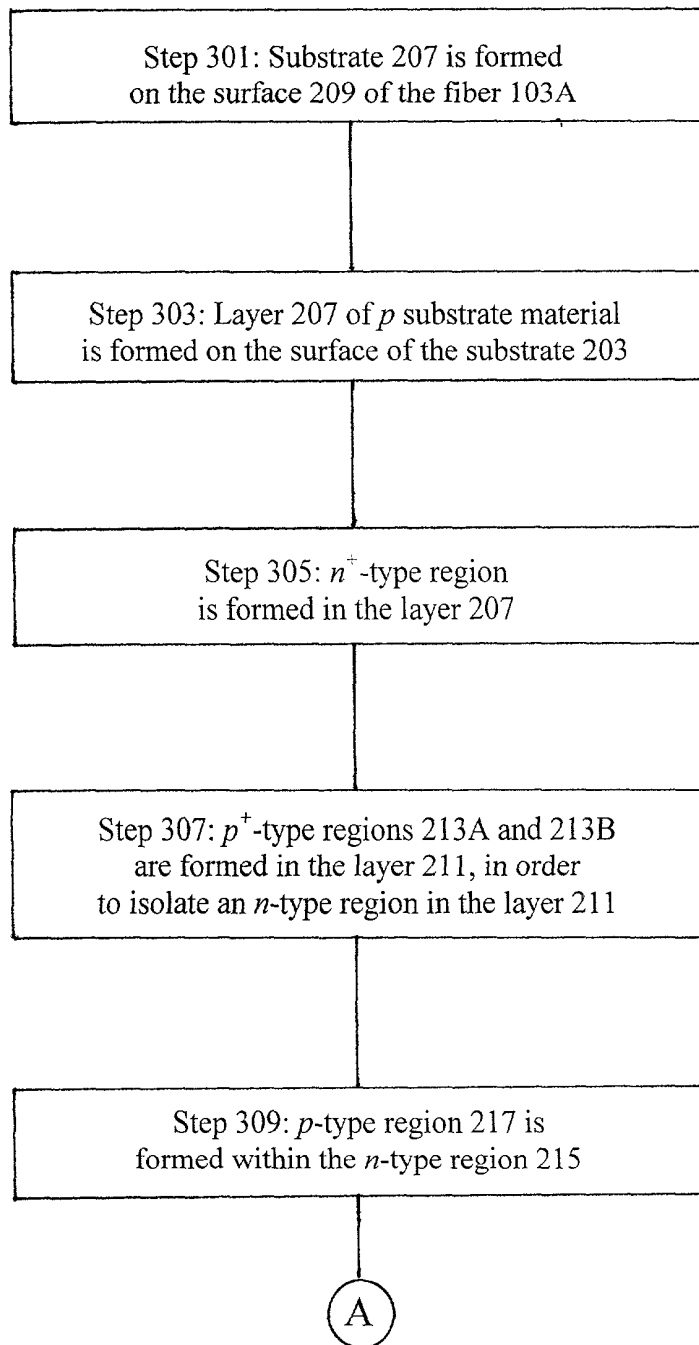


FIG. 3A

A

Step 311:  $n^+$ -type region 219 is formed in  $n$ -type region 215 and an  $n^+$ -type region 221 is formed in  $p$ -type region 217

Step 313: Electrodes 223A, 223B and 223C are formed

Step 315: Protective layer 225 is formed over the transistor 201

Step 317: Transistor 201 is covered with a shielding layer 227

FIG. 3B

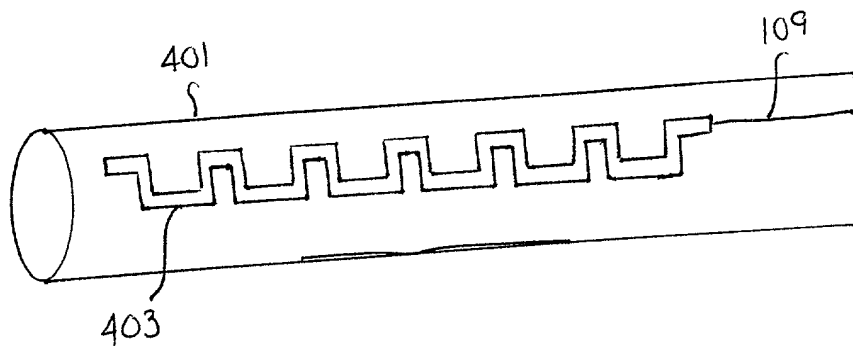


FIG. 4

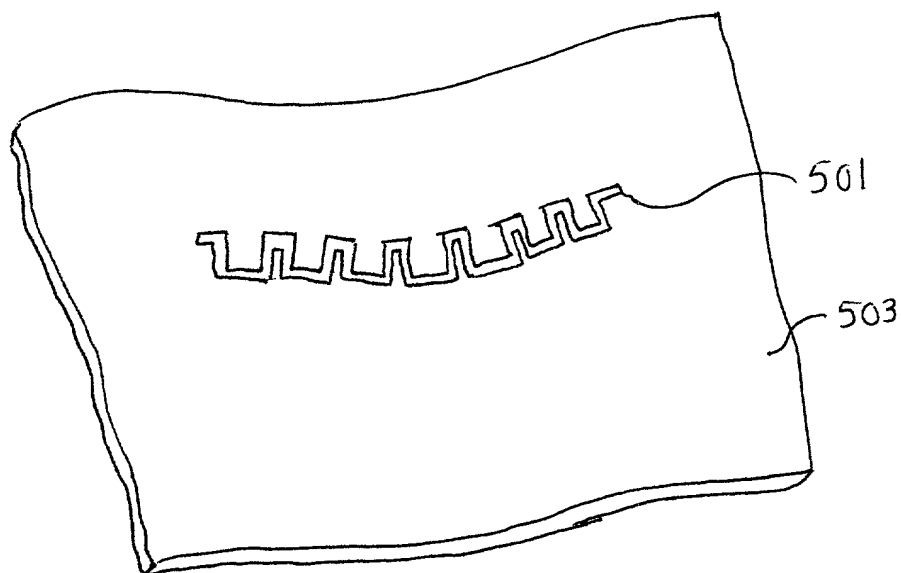


FIG. 5